

100

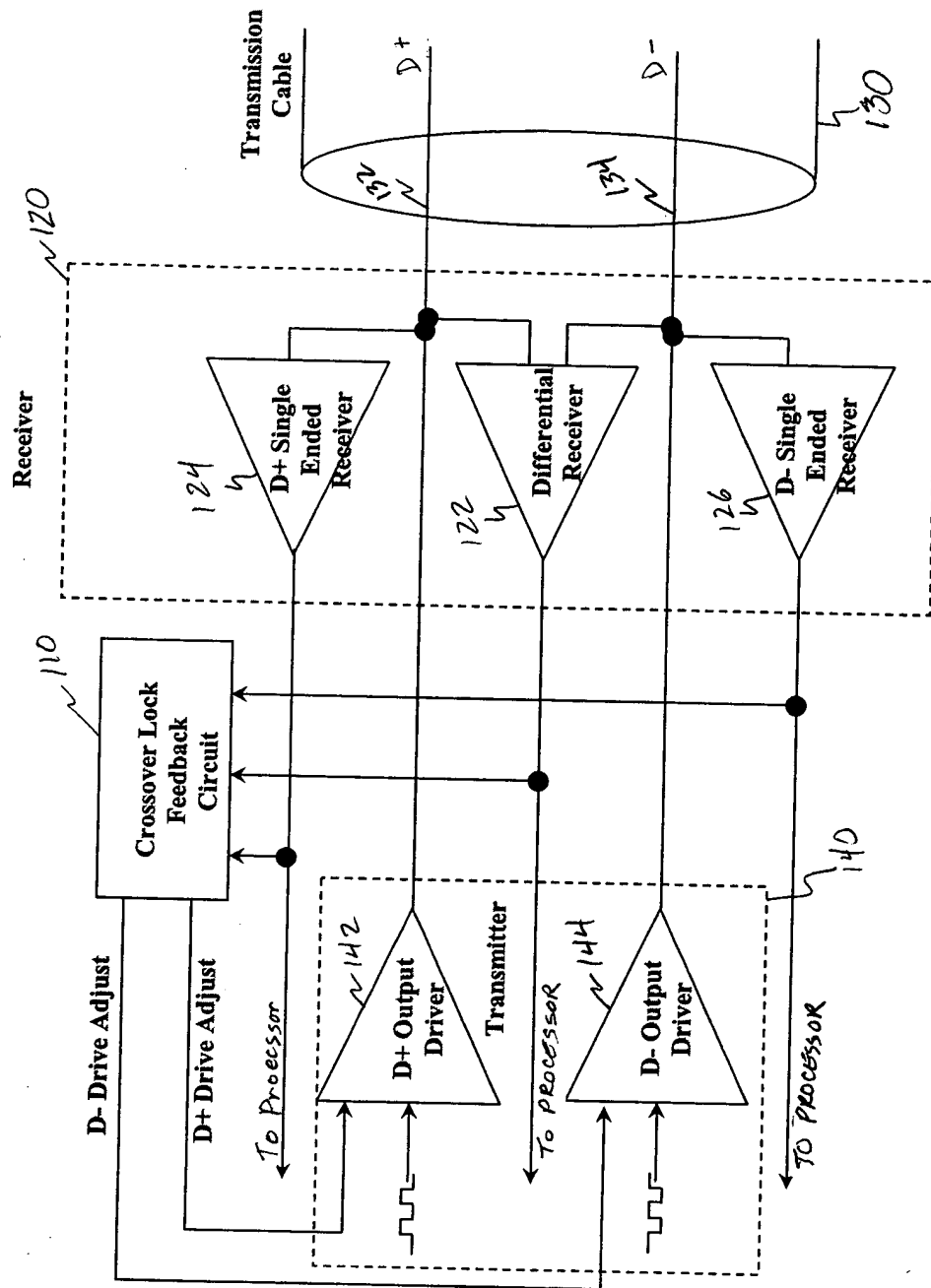
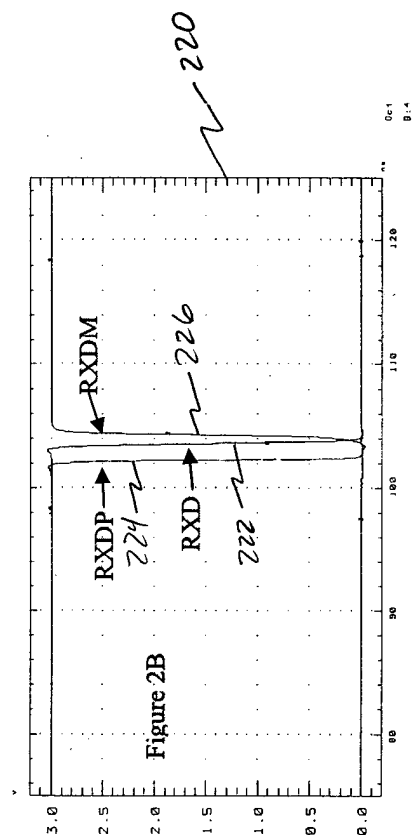
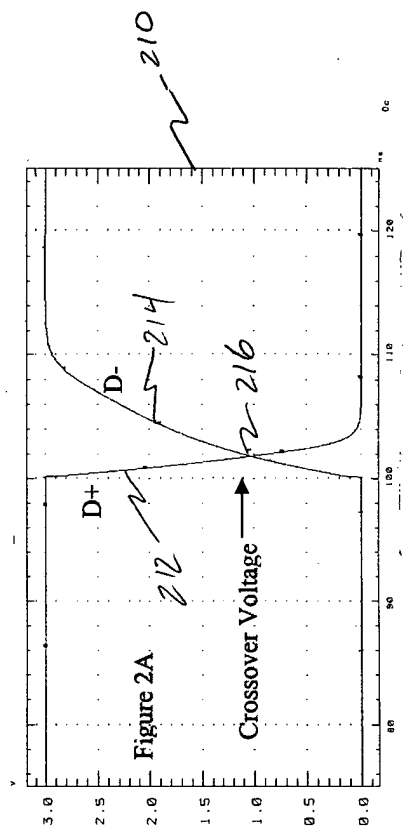


FIG. 1



$$\begin{aligned}
 & \text{RXD} \cdot \text{RXDP} \rightarrow \text{K} \\
 & \text{RXD} \cdot \text{RXDM} \rightarrow \text{K} \\
 & \text{RXD} \cdot \text{RXDM} \rightarrow \text{K} \\
 & \text{RXD} \cdot \text{RXDM} \rightarrow \text{K}
 \end{aligned}$$

230

240

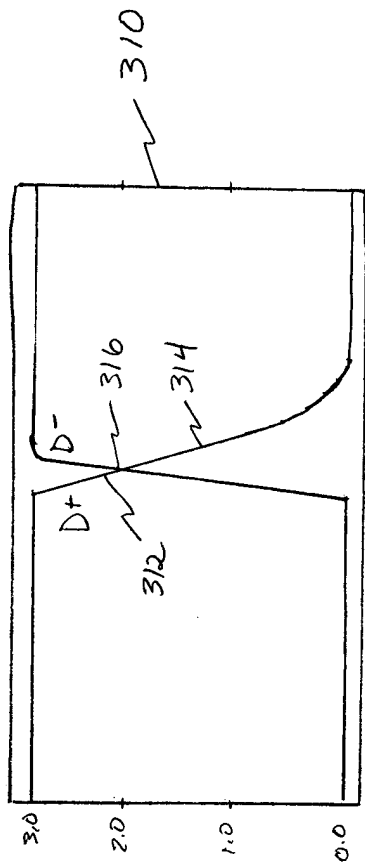


FIG. 3A

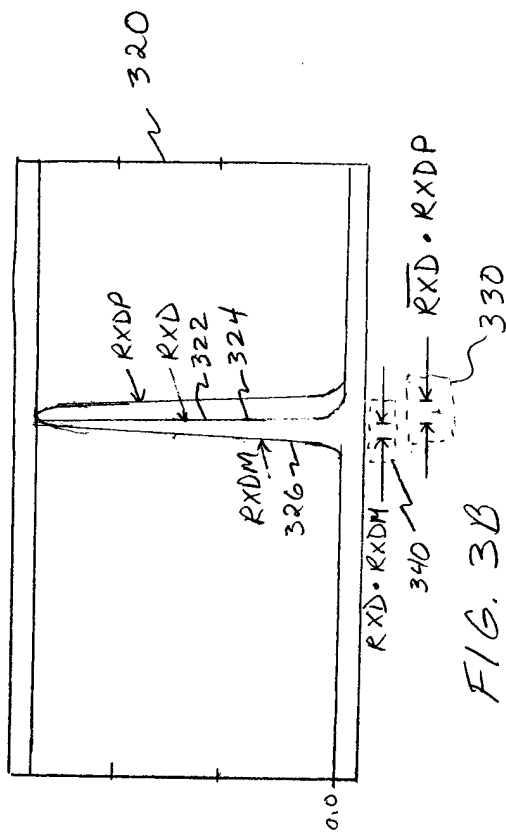


FIG. 3B

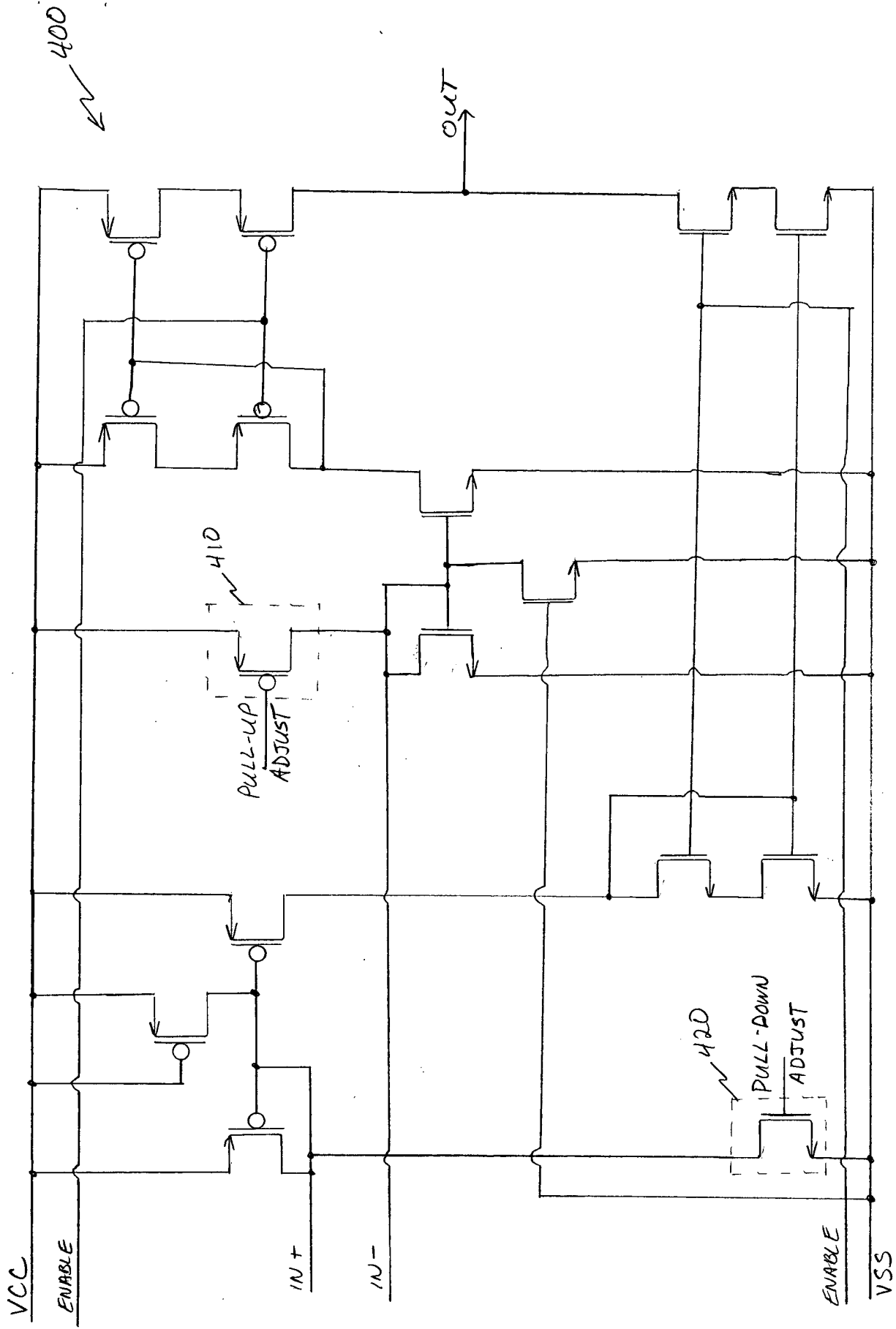


FIG. 4

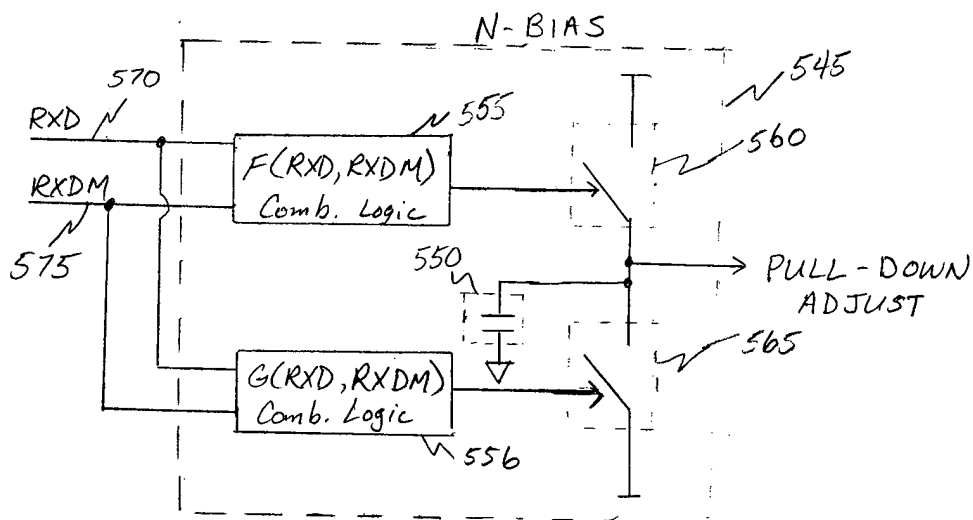
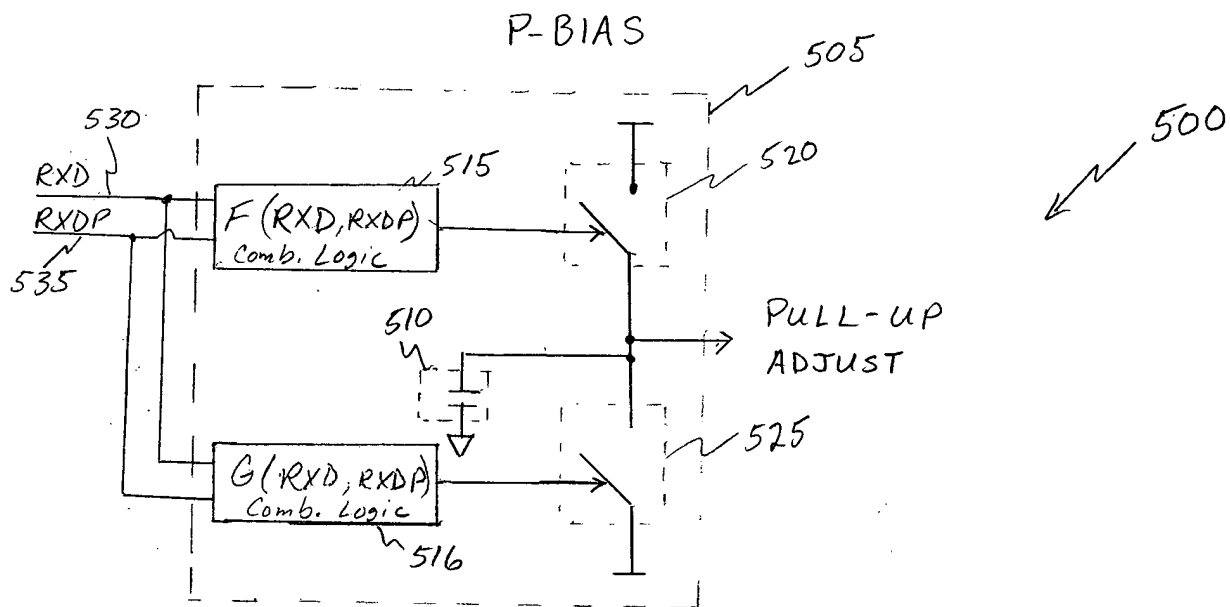


FIG. 5

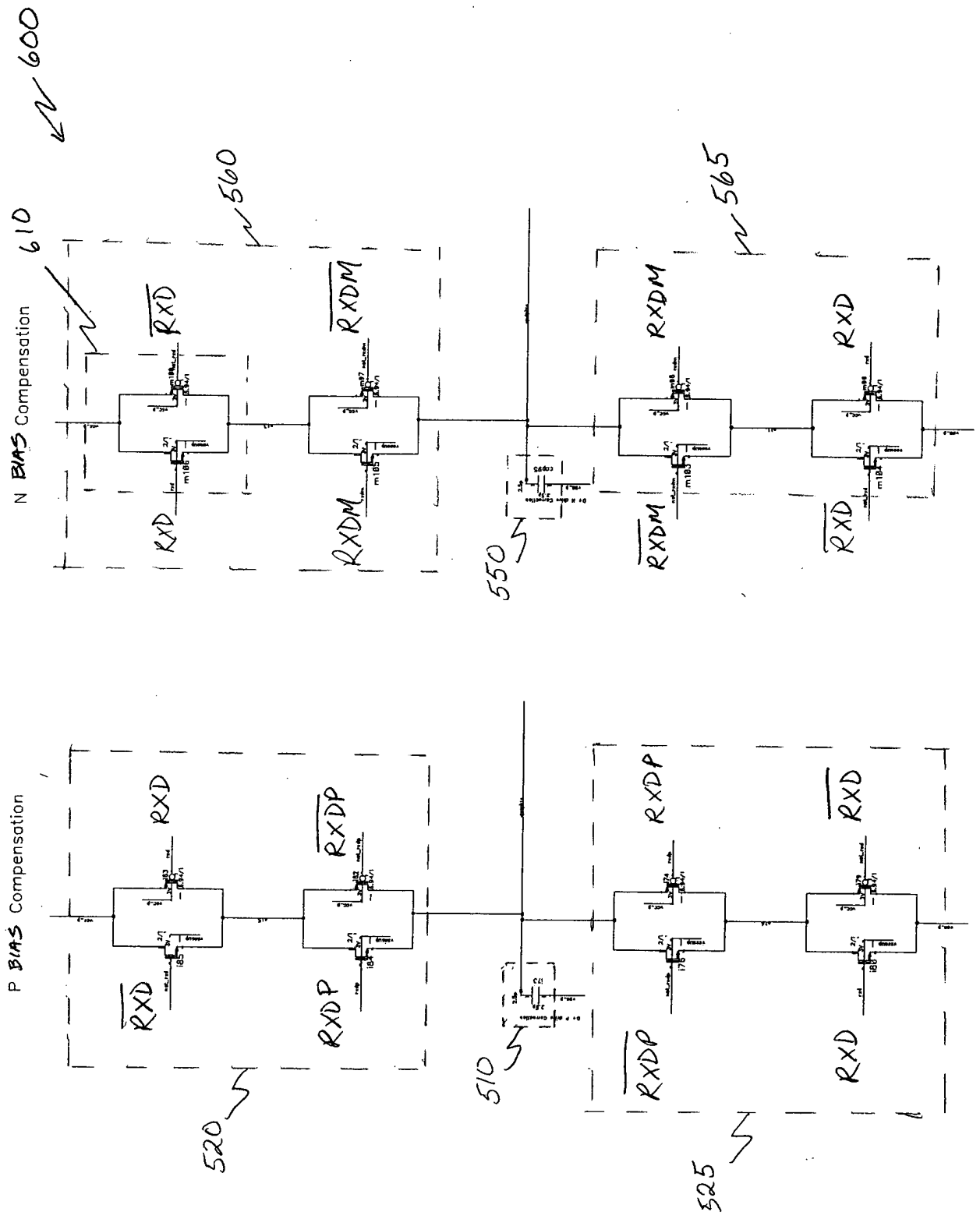


FIG. 6

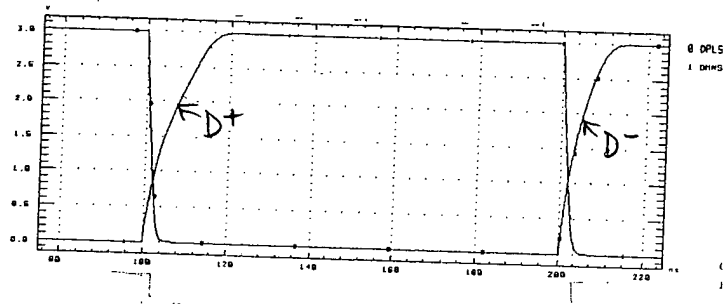


FIG. 7A

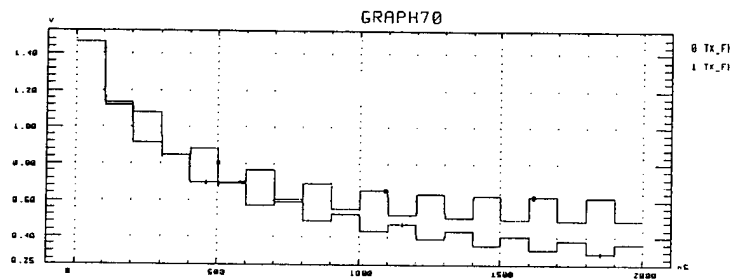


FIG. 7B

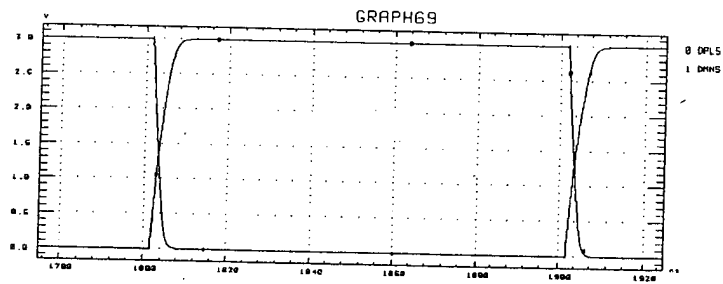


FIG. 7C

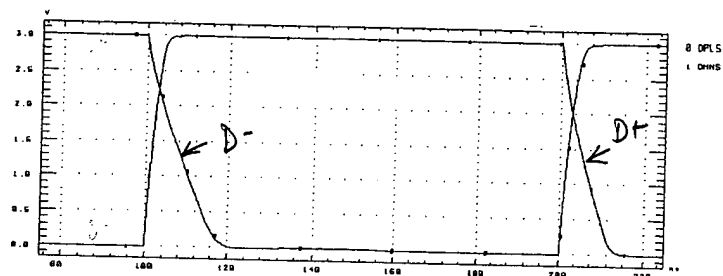


FIG. 8A

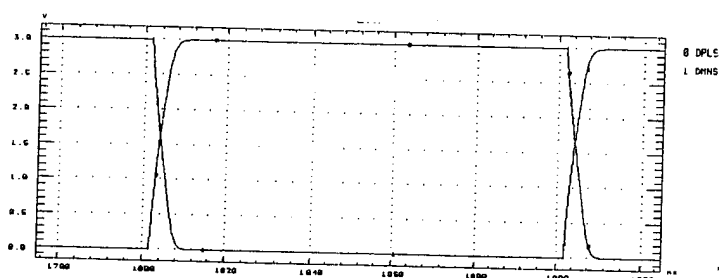


FIG. 8B

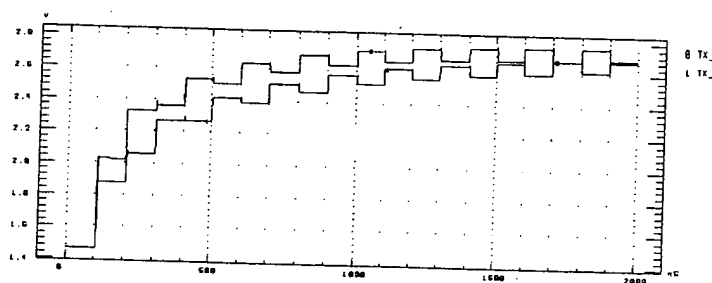


FIG. 8C



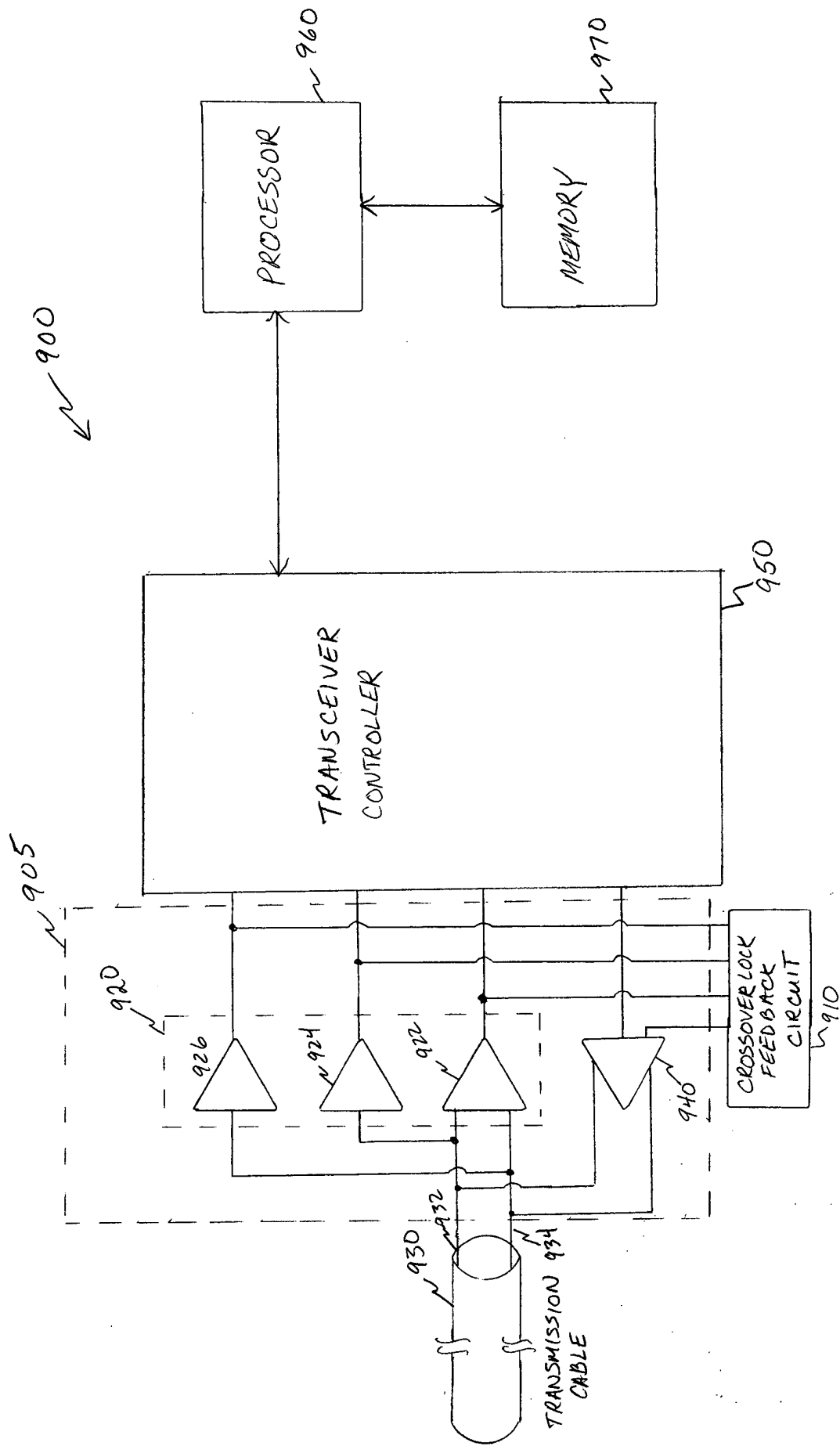


FIG. 9

MEASURING A DIFFERENCE BETWEEN A VOLTAGE AT WHICH OUTPUT VOLTAGES OF FIRST AND SECOND DIFFERENTIAL DRIVERS OF A DIFFERENTIAL SIGNAL TRANSCIEVER CROSS-OVER AND A POINT SUBSTANTIALLY EQUIDISTANT BETWEEN MAXIMUM AND MINIMUM OUTPUT VOLTAGES

1000

1010

PROVIDING A CORRECTING BIAS VOLTAGE PROPORTIONAL TO A DIFFERENCE BETWEEN THE CROSS-OVER VOLTAGE AND THE EQUIDISTANT VOLTAGE

1020

APPLYING THE CORRECTING BIAS VOLTAGE TO THE DIFFERENTIAL DRIVERS TO VARY THE POINT WHERE THE FIRST AND SECOND VOLTAGES CROSS-OVER

1030

FIG. 10